Search Notes					

Application/Control No.	Applicant(s)/Patent under Reexamination
10/084,515	BURNLEY, RICHARD P.
Examiner	Art Unit
Leigh Marie Garbowski	2825

SEARCHED '				
Class	Subclass	Date	Examiner	
716	6,17	2/2/2005	LMG	
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INTERFERENCE SEARCHED				
Class	Subclass	Date	Examiner	
716	6,17	3/5	Ms	
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(INCLUDING SEARCH	STRATEGY)
	DATE	EXMR
WEST	†	<u> </u>
timing	1	
core embodded	1	
embedded pld	1	
clock-to-output	1	
setup		
hold	2/2/2005	LMG
delay	1	ļ
path		
portion memory controller	1	
fgpa	1	
host	1	
feeding back	1	
modifications		
(terms were also varied, i.e. set-up,	2/2/2005	LMG
modifying, path delay, etc.)	222003	
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